

5711-2D

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09/978141
10/15/01

Class	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

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SCANNED C84 Q.A. Ar

APPLICATION NO. 09/978141	CONT/PRIOR	CLASS 385 <i>416</i>	SUBCLASS <i>719</i>	ART UNIT <i>2825</i> 2818 <i>2829</i>	EXAMINER <i>Dmyan</i>
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APPLICANTS

Yaron Kretchmer
Michael Porter
Thomas O'Brien

TITLE

Automatic method and system for instantiating built-in -test (BIST) modules in ASIC memory designs

PTO-2040
12/89

ISSUING CLASSIFICATION

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div> <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div> </div>		CLAIMS ALLOWED	
			Total Claims.	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED 	
	_____ (Priamry Examiner) (Date)		ISSUE FEE	
			Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	

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